U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/772027	F	~~226~~ 712	226	~~2893~~ 2183	Trent code

APPLICANTS

Kenji Shigeki
Ryohei Tanaka
Toshimitsu Nakao

TITLE

Logic integrated circuit, and recording medium readable by a computer, which stores a source of CPU core on said logic integrated circuit

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

(Assistant Examiner) _____ (Date)

NOTICE OF ALLOWANCE MAILED

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

(Primary Examiner) _____ (Date)

ISSUE FEE

Amount Due	Date Paid

☐ The terminal ____ months of this patent have been disclaimed.

(Legal Instruments Examiner) _____ (Date)

ISSUE BATCH NUMBER

Form PTO-436A
(Rev. 8/99)

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